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Serial No. 10/808,635

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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|-------------|-------------------------------------|-----------------|----------------------|
| Applicant: | Siew Siong Teo | Examiner: | Not yet assigned |
| Serial No.: | 10/808,635 | Group Art Unit: | 2838 |
| Filed: | March 24, 2004 | Docket No.: | 50019.273US01/P05808 |
| Title: | CURVATURE CORRECTED BANDGAP CIRCUIT | | |

CERTIFICATE UNDER 37 CFR 1.8:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on August 10, 2004.

By: 

Name: Brett A. Hertzberg

INFORMATION DISCLOSURE STATEMENT (37 C.F.R. § 1.97(b))

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

With regard to the above-identified application, the items of information listed on the enclosed Form 1449 are brought to the attention of the Examiner.

This statement should be considered because it is submitted before the mailing of a first Office Action after the filing of a Request for Continued Examination under 37 C.F.R. § 1.114 or a CPA under 37 C.F.R. § 1.53(d). Accordingly, no fee is due for consideration of the items listed on the enclosed Form 1449.

A copy of any foreign patent document or "Other Document" listed on the Form 1449 is enclosed, in accordance with 37 C.F.R. § 1.98(a)(2). Because this application was filed after June 30, 2003, copies of the U.S. Patents and U.S. patent publications listed on the enclosed Form 1449 are not provided.

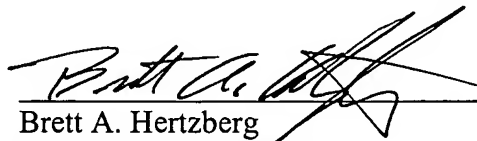
No representation is made that a reference is "prior art" within the meaning of 35 U.S.C. §§ 102 and 103 and Applicant reserves the right, pursuant to 37 C.F.R. § 1.131 or otherwise, to establish that the reference(s) are not "prior art." Moreover, Applicant does not represent that a reference has been thoroughly reviewed or that any relevance of any portion of a reference is intended.

Consideration of the items listed is respectfully requested. Pursuant to the provisions of M.P.E.P. 609, it is requested that the Examiner return a copy of the attached Form 1449, marked as being considered and initialed by the Examiner, to the undersigned with the next official communication.

Please charge any additional fees or credit any overpayment to Deposit Account No. 13-2725.

Respectfully submitted,

MERCHANT & GOULD



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| Filed: | March 24, 2004 | Docket: | 50019.273US01/P05808 |
| Confirmation No.: | 6093 | | |
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By: 
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Commissioner for Patents
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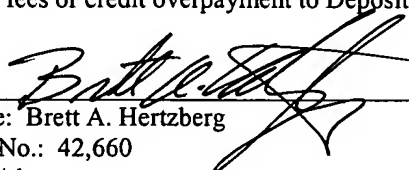
Sir:

We are transmitting herewith the attached:

- ☒ Transmittal Sheet in duplicate containing Certificate of Mailing
- ☒ Information Disclosure Statement, Form 1449, 5 Reference(s)
- ☒ Return postcard


Please consider this a PETITION FOR EXTENSION OF TIME for a sufficient number of months to enter these papers or any future reply, if appropriate. Please charge any additional fees or credit overpayment to Deposit Account No. 13-2725. A duplicate of this sheet is enclosed.

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206.342.6200

By: 
Name: Brett A. Hertzberg
Reg. No.: 42,660
BAH/ab

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PATENT TRADEMARK OFFICE

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| FORM 1449* INFORMATION DISCLOSURE STATEMENT  IN AN APPLICATION (Use several sheets if necessary) | Docket Number: 50019.273US01/P05808 | Application Number: 10/808,635 |
| | Applicant: Siew Siong Teo | |
| | Filing Date: March 24, 2004 | Group Art Unit: 2838 |

U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | DOCUMENT NO. | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
|---------------------|-----------------|---------|-----------------|-------|----------|-------------------------------|
| | 6,075,354 | 6/13/00 | Smith et al. | 323 | 313 | |
| | US 6,183,131 B1 | 2/6/01 | Holloway et al. | 374 | 172 | |
| | US 6,232,828 B1 | 3/15/01 | Smith et al. | 327 | 539 | |
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FOREIGN PATENT DOCUMENTS

| | DOCUMENT NO. | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION | |
|--|--------------|------|---------|-------|----------|-------------|----|
| | | | | | | YES | NO |
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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|--|--|--|
| | | Randall L. Geiger et al., "VLSI Design Techniques for Analog and Digital Circuits," 1990 (2 pages) |
| | | Phillip E. Allen et al., "CMOS Analog Circuit Design," 1987 (3 pages) |
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PATENT TRADEMARK OFFICE

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|---|-----------------|
| EXAMINER | DATE CONSIDERED |
| EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant. | |